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## N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Luk et al.

Docket No.:

Serial No.:

YOR920030603US1 10/751,714

Filed:

January 5, 2004

Group:

2826

Examiner:

J.P. Mondt

Title:

Amplifier Using Gated Diodes

## TRANSMITTAL OF FORMAL DRAWINGS

1450, Alexandria, VA 22313-1450

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

Applicants submit herewith twenty (20) sheets of formal drawings in the abovereferenced application.

Respectfully submitted,

Date: December 13, 2005

Kevin M. Mason

Attorney for Applicants

Reg. No. 36,597

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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**Amplifiers Using Gated Diodes** 

## TRANSMITTAL LETTER

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent

application:

- 1. Amendment and Response to Office Action;
- 2. Substitute Formal Drawings 2B, 5B, 7 and 9;
- 3. Marked up Figures 2B, 5B, 7 and 9;
- 4. Transmittal of Formal Drawings; and
- 5. Formal Drawings- 20 sheets

There is no additional claim fee due in connection with the Response.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit IBM Corporation Deposit Account No. 50-0510 as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,

Dated: December 13, 2005

Attorney for Applicant(s)

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